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January 16, 2007

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Harold C. Moore

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Signature

January 16, 2007

Date of Signature

Re: Application of:

Serial No.:

Charles E. May

09/943,196

Filed: Confimration No.: August 30, 2001 8500

For:

Arrangement and Method for

Fabricating a Semiconductor Wafer

1765

Group Art Unit:

Examiner:

Lynette T. Umez Eronini

Our Docket No.:

1003-0606 (01-146)

PETITION FOR EXTENSION OF TIME **PURSUANT TO 37 C.F.R. § 1.136(a)**

Sir:

In connection with the Office Action mailed September 14, 2006 in the aboveentitled patent application, Applicant respectfully requests a one (1) month extension in which to file a response, from December 14, 2006 to January 14, 2007.

Since the due date for filing the Response to Office Action fell on Sunday, January 14, 2007, and Monday, January 15, 2007 was a federal holiday, the Response is being timely filed on Tuesday, January 16, 2007.

01/19/2007 JBALINAN 00000038 122252 09943196

01 FC:1251

120.00 DA

The \$120.00 fee required by 37 C.F.R. § 1.17(a)(1) is submitted herewith.

Respectfully Submitted,

Harold C. Moore

Attorney for Applicant Registration No. 37,892

January 16, 2007 Maginot, Moore & Beck, LLP Chase Tower 111 Monument Circle, Suite 3250 Indianapolis, IN 46204-5109 (317) 638-2922